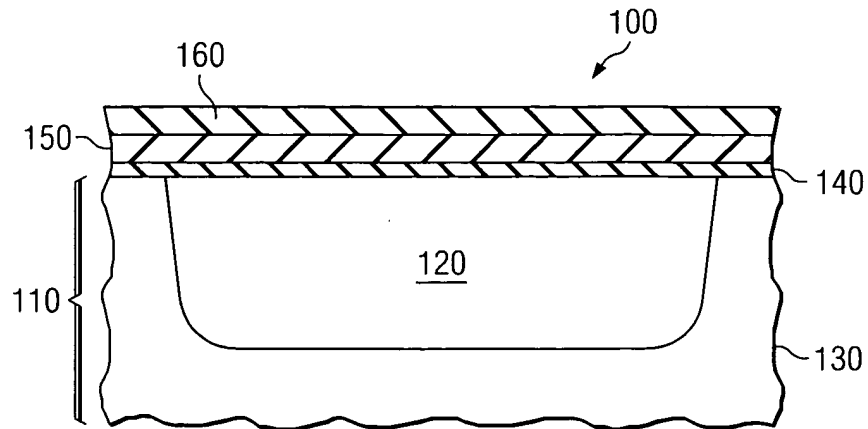
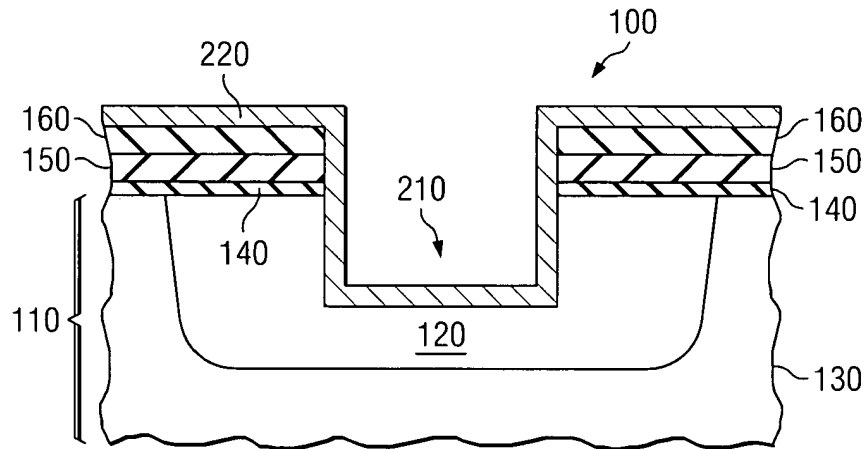


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METHOD AND STRUCTURE IMPROVING ISOLATION  
BETWEEN MEMORY CELL PASSING GATE AND CAPACITOR

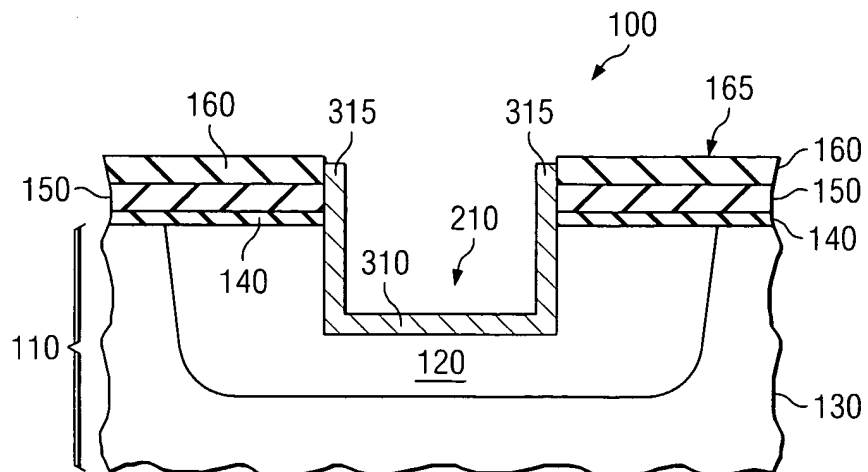
1/5



*Fig. 1*

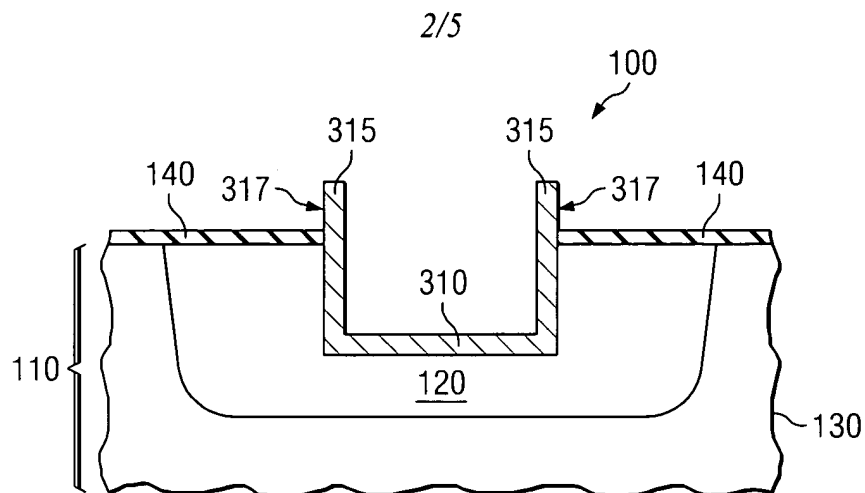


*Fig. 2*

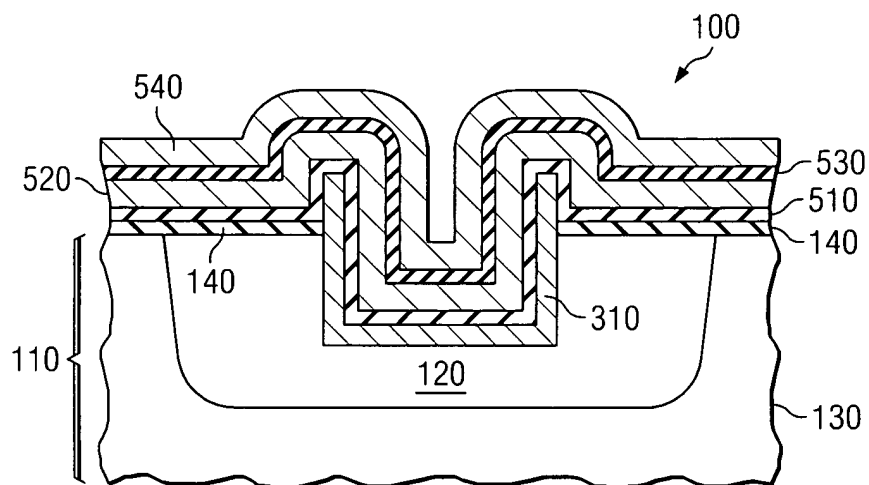


*Fig. 3*

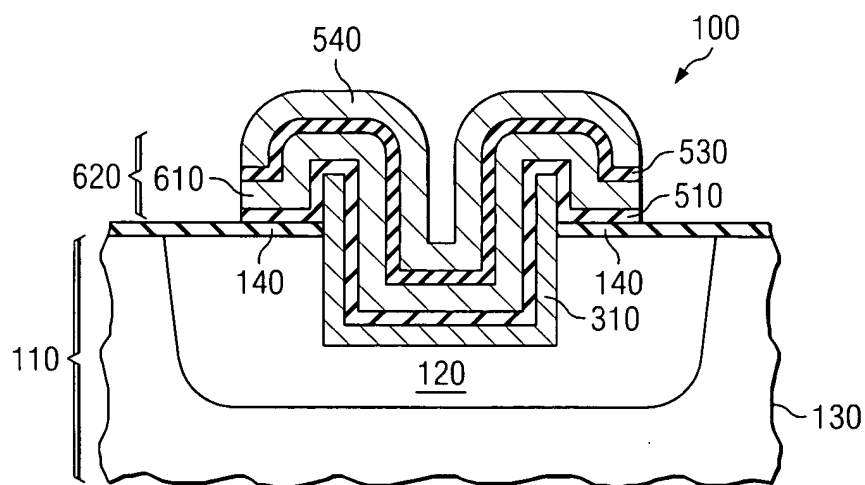
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 METHOD AND STRUCTURE IMPROVING ISOLATION  
 BETWEEN MEMORY CELL PASSING GATE AND CAPACITOR



*Fig. 4*



*Fig. 5*



*Fig. 6*

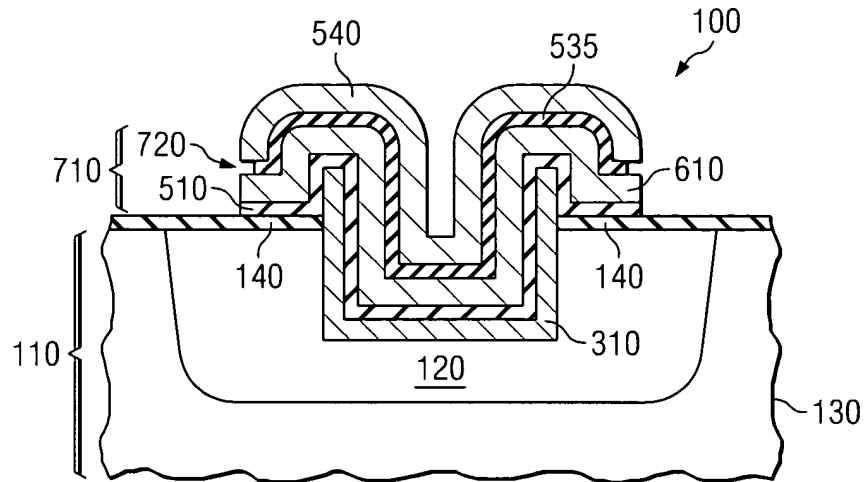


Fig. 7

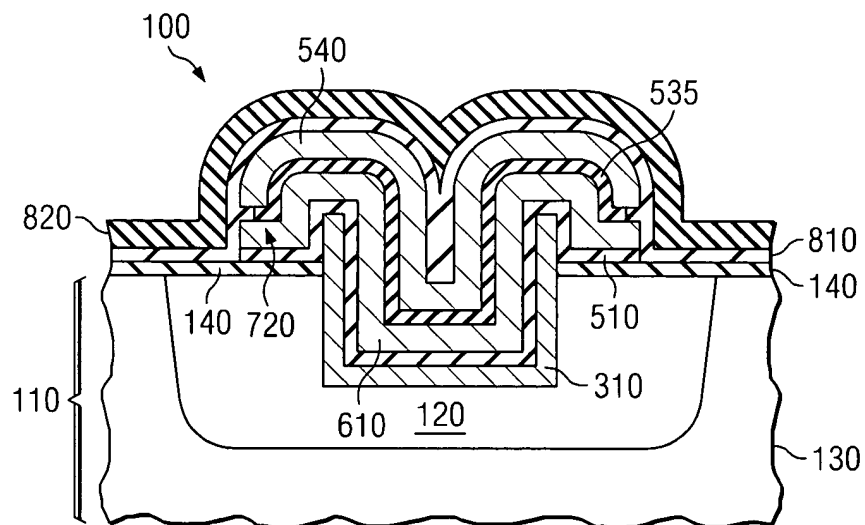
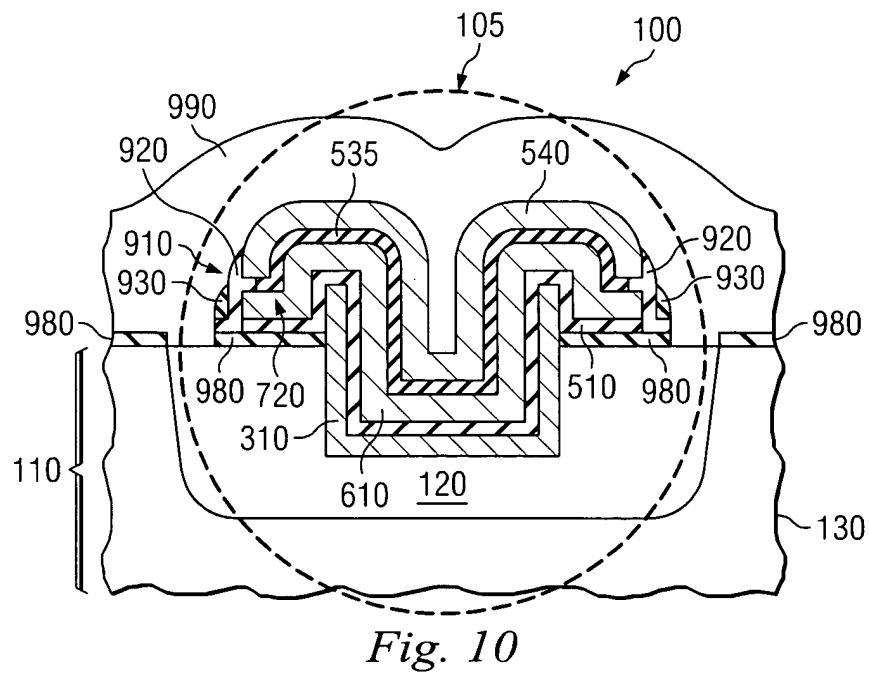
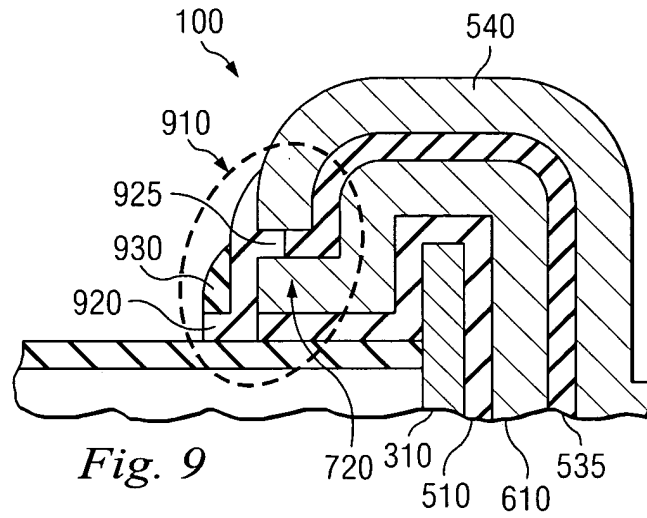


Fig. 8



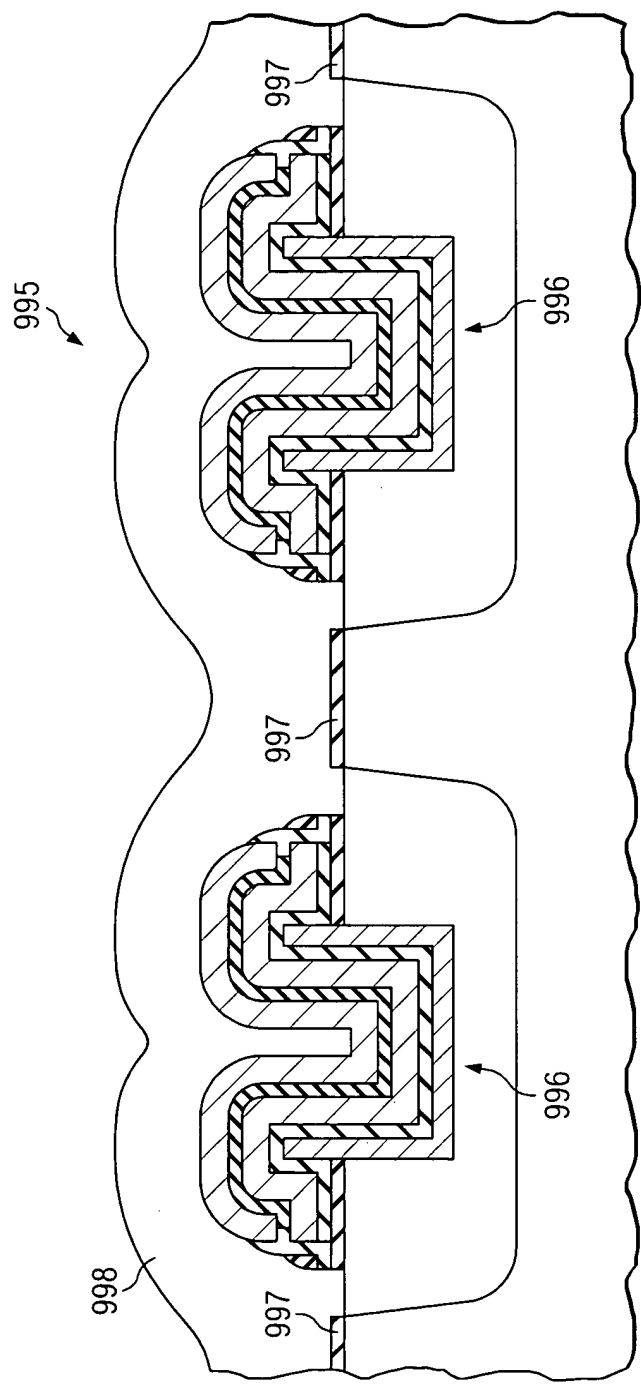


Fig. 11